## The RF Line NPN Silicon RF Power Transistor

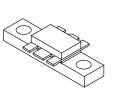
The TP3032 is designed for 26 volts, common emitter, 960 MHz base station amplifiers, for use in analog and digital systems.

- Specified 26 Volts, 960 MHz Characteristics Output Power — 21 Watts Gain — 7.5 dB min
- Silicon Nitride Passivated
- Gold Metallized, Emitter Ballasted for Long Life and Resistance to Metal Migration
- Class AB Operation

MAXIMUM RATINGS

• Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

		-	
Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCER	40	Vdc
Collector-Base Voltage	VCBO	48	Vdc
Emitter-Base Voltage	VEBO	3.5	Vdc
Collector-Current - Continuous	ιc	4	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	52.5 0.3	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	– 65 to +150	°C
Operating Junction Temperature	Тј	200	°C



**TP3032** 

21 W, 960 MHz RF POWER TRANSISTOR

NPN SILICON

CASE 319-07, STYLE 2

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case (1)		3.3	°C/W

**ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage $(I_C = 30 \text{ mA}, R_{BE} = 75 \Omega)$	V(BR)CER	40	—	—	Vdc
Emitter-Base Breakdown Voltage (IE = 5 mAdc)	V(BR)EBO	3.5	_	—	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 30 mAdc)	V(BR)CBO	48	—	—	Vdc
Collector–Emitter Leakage ( $V_{CE} = 26 \text{ V}, \text{ R}_{BE} = 75 \Omega$ )	ICER	_	_	8	mA
ON CHARACTERISTICS					
DC Current Gain (I <sub>C</sub> =1 Adc, V <sub>CE</sub> = 10 Vdc)	hFE	15	_	80	_

NOTE:

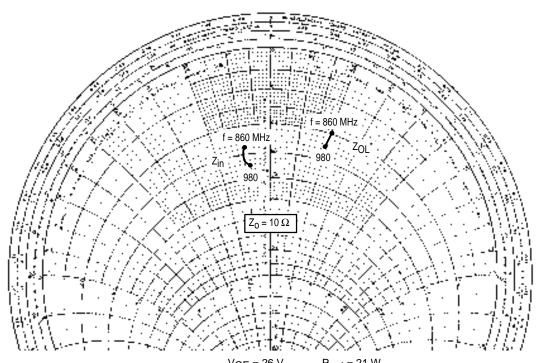
1. Thermal resistance is determined under specified RF operating condition.



(continued)

## **ELECTRICAL CHARACTERISTICS** — continued ( $T_C = 25^{\circ}C$ unless otherwise noted.)

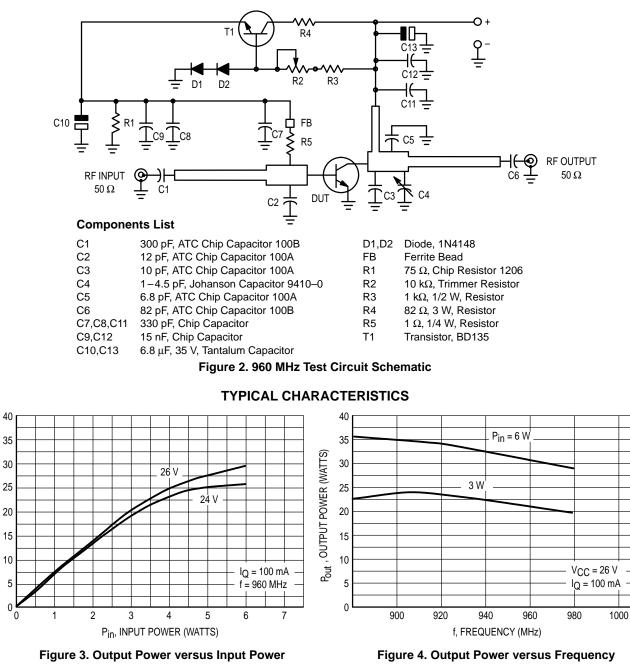
Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS					
Output Capacitance ( $V_{CB} = 26 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ )	C <sub>ob</sub>	_	30	—	pF
FUNCTIONAL TESTS					
Common–Emitter Amplifier Gain (V <sub>CC</sub> = 26 V, P <sub>out</sub> = 21 W, I <sub>CQ</sub> = 100 mA, f = 960 MHz)	Gp	7.5	8.5	-	dB
Load Mismatch $(V_{CC} = 26 \text{ V}, P_{out} = 21 \text{ W}, I_{CQ} = 100 \text{ mA}, \text{ Load VSWR} = 5:1, at All Phase Angles at Frequency of Test}$	Ψ	No Degradation in Output Power			
Collector Efficiency (V <sub>CC</sub> = 26 V, P <sub>out</sub> = 21 W, f = 960 MHz)	η	50	55	_	%
Over Drive (V <sub>CC</sub> = 26 V, P <sub>in</sub> = 6 W, f = 960 MHz)	OD	No Degradation in Output Power			

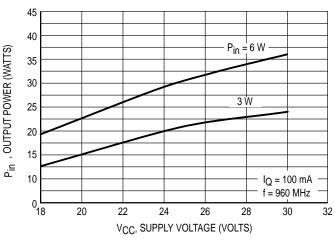


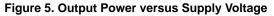
	VCE = 26 V	$P_{out} = 21 \text{ VV}$
f (MHz)	<b>Z<sub>in</sub></b> (Ω)	Z <sub>OL</sub> * (Ω)
860	2.9 – j0.4	2 + j2.2
880	2.9 – j0.9	2.1 + j2.2
900	2.9 – j1.45	2.25 + j2.5
935	3.2 – j0.95	2.4 + j2.3
960	3.25 – j1.5	2.5 + j2
980	3.55 – j1.1	2.6 + j2.15

Z<sub>OL</sub>\* = Conjugate of optimum load impedance into which the device operates at a given output power, voltage, current and frequency.

Figure 1. Series Equivalent Input and Output Impedances







Pout, , OUTPUT POWER (WATTS)

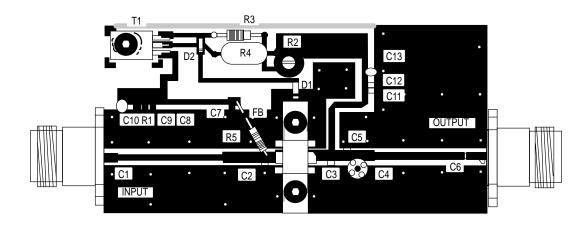
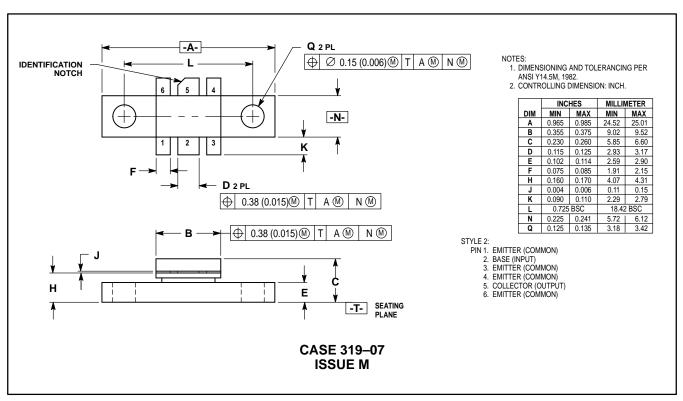


Figure 6. Test Circuit Components View



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