

# SIGC223T120R2CS

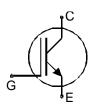
# IGBT Chip in NPT-technology

## FEATURES:

- 1200V NPT technology 175µm chip
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling
- integrated gate resistor

## This chip is used for:

- IGBT Modules
- Applications:
- drives, SMPS, resonant applications



Chip Type	V <sub>CE</sub>	<b>I</b> Cn	Die Size	Package	Ordering Code
SIGC223T120R2CS	1200V	150A	14.4 x 15.5 mm <sup>2</sup>	sawn on foil	tbd

## MECHANICAL PARAMETER:

Raster size	14.4 X 15.5	mm <sup>2</sup>			
Emitter pad size	8x( 3.67x6.77 )				
Gate pad size	1.49 x 1.51				
Area total / active	223.5 / 189.9				
Thickness	175	μm			
Wafer size	150	mm			
Flat position	90	grd			
Max.possible chips per wafer	54 pcs				
Passivation frontside	Photoimide				
Emitter metallization	3200 nm Al Si 1%				
Collector metallization 1400 nm Ni Ag –system suitable for epoxy and soft solder die bo					
Die bond	electrically conductive glue or solder				
Wire bond	e bond Al, <500µm				
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



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### MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, Tj=25 °C	V <sub>CE</sub>	1200	V
DC collector current, limited by T <sub>jmax</sub>	I <sub>C</sub>	1)	А
Pulsed collector current, t <sub>p</sub> limited by T <sub>jmax</sub>	I <sub>cpuls</sub>	450	А
Gate emitter voltage	V <sub>GE</sub>	±20	V
Operating junction and storage temperature	T <sub>j</sub> , T <sub>stg</sub>	-55 +150	°C

<sup>1)</sup> depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip),  $T_j$ =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
	Cymbol	Conditions	min.	typ.	max.	•
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	$V_{GE}$ =0V , I <sub>C</sub> =4mA	1200			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	V <sub>GE</sub> =15V, I <sub>C</sub> =150A	2.7	3.2	3.7	V
Gate-emitter threshold voltage	V <sub>GE(th)</sub>	$I_C=6mA$ , $V_{GE}=V_{CE}$	4.5	5.5	6.5	
Zero gate voltage collector current	I <sub>CES</sub>	$V_{CE}$ =1200V , $V_{GE}$ =0V			900	μA
Gate-emitter leakage current	I <sub>GES</sub>	$V_{CE}=0V$ , $V_{GE}=20V$			600	nA
Integrated gate resistor	R <sub>Gint</sub>		1.75	2	3.25	Ω

# **ELECTRICAL CHARACTERISTICS** (tested at component):

Parameter	Symbol	Conditions	Value			Unit
Falameter	Symbol	Conditions	min.	typ.	max.	Unit
Input capacitance	Ciss	V <sub>CE</sub> =25V,	-	9.3		nF
Output capacitance	Coss	$V_{GE}=0V$ ,	-	1.4		
Reverse transfer capacitance	Crss	<i>f</i> =1MHz	-	0.7		

# SWITCHING CHARACTERISTICS (tested at component), Inductive Load

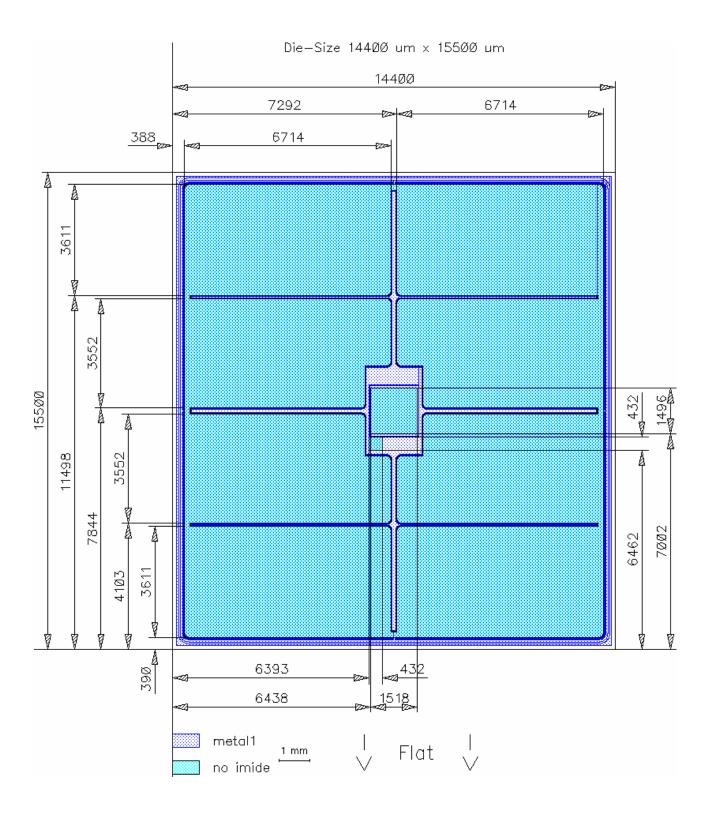
Parameter	Symbol	Conditions <sup>1)</sup>	Value			Unit
T diameter	Oymbol		min.	typ.	max.	Onic
Turn-on delay time	t <sub>d(on)</sub>	<i>T</i> <sub>j</sub> =125°C	-	125		ns
Rise time	t <sub>r</sub>	$V_{\rm CC} = 600 V$ ,	-	100		
Turn-off delay time	$t_{d(off)}$	ν <sub>GE</sub> =-15/15V,	-	590		
Fall time	t <sub>f</sub>	$R_{\rm G}$ =6.8 $\Omega$	-	70		

<sup>1)</sup> values also influenced by parasitic L- and C- in measurement and package.





## **CHIP DRAWING:**





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## FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the	thd
device data sheet	tbd

### **DESCRIPTION:**

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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