1A LOW DROPOUT POSITIVE ADJUSTABLE REGULATOR

PRELIMINARY DATASHEET

FEATURES

- **Guaranteed < 1.3V Dropout at Full Load Current**
- **Fast Transient Response**
- **1% Voltage Reference Initial Accuracy**
- Built-in Thermal Shutdown
- **Available in SOT-223, D-PAK , Power Flex and 8 pin SOIC Surface Mount Packages**

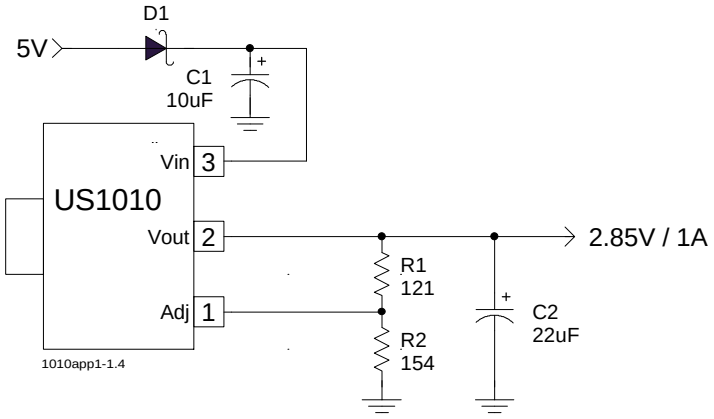
APPLICATIONS

- VGA & Sound Card Applications
- Low Voltage High Speed Termination Applications
- Standard 3.3V Chip-Set and Logic Applications

DESCRIPTION

The US1010 is a low dropout three terminal adjustable regulator with minimum of 1A output current capability. This product is specifically designed to provide well regulated supply for low voltage IC applications such as **high speed bus termination and low current 3.3V logic supply**. The US1010 is also well suited for other applications such as VGA and sound cards. The US 1010 is **guaranteed to have <1.3V drop out at full load current** making it ideal to provide well regulated outputs of 2.5V to 3.6V with 4.75V to 7V input supply.

TYPICAL APPLICATION



Typical Application of US1010 in a 5V to 2.85V SCSI termination regulator .

PACKAGE ORDER INFORMATION

Tj (°C)	2 PIN PLASTIC TO252 (D)	3 PIN PLASTIC SOT223 (Y)	2 PIN PLASTIC POWER FLEX (P)	8 PIN PLASTIC SOIC (S)
0 TO 150	US1010CD	US1010CY	US1010CP	US1010CS

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Vin) 7V
Power Dissipation ..... Internally Limited
Storage Temperature Range ..... -65°C TO 150°C
Operating Junction Temperature Range ..... 0°C TO 150°C

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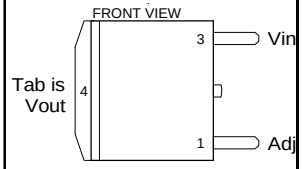	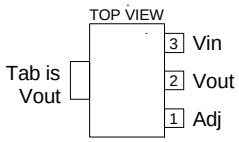	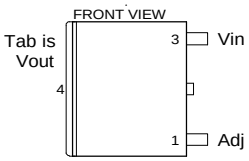	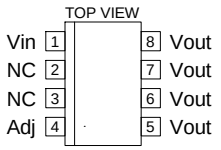
θ_{JA}=70°C/W for 0.5" Sq pad	θ_{JA}=90°C/W for 0.4" Sq pad	θ_{JA}=70°C/W for 0.5" Sq pad	θ_{JA}=55°C/W for 1" Sq pad

ELECTRICAL SPECIFICATIONS

Unless otherwise specified, these specifications apply over, Cin=1uF, Cout=10uF, and Tj=0 to 150°C.Typical values refer to Tj=25°C.

PARAMETER	SYM	TEST CONDITION	MIN	TYP	MAX	UNITS
Reference Voltage	V_{REF}	Io=10mA,Tj=25°C,(Vin-Vo)=1.5V	1.243	1.250	1.257	V
		Io=10mA, (Vin-Vo)=1.5V	1.237	1.250	1.263	
Line Regulation		Io=10mA,1.3V<(Vin-Vo)<7V			0.2	%
Load Regulation (note 1)		Vin=3.3V,Vadj=0,10mA<Io<1A			0.4	%
Dropout Voltage (note 2)	ΔV_O	Note 2 , Io=1A		1.1	1.3	V
Current Limit		Vin=3.3V,dVo=100mV	1.1			A
Minimum Load Current (note 3)		Vin=3.3V,Vadj=0V		5	10	mA
Thermal Regulation		30 mS PULSE,Vin-Vo=3V,Io=1A		0.01	0.02	%/W
Ripple Rejection		f=120HZ ,Co=25uF Tan				
		Io=0.5A,Vin-Vo=3V	60	70		dB
Adjust Pin Current	I_{ADJ}	Io=10mA,Vin-Vo=1.5V,Tj=25				
		Io=10mA,Vin-Vo=1.5V		55	120	uA
Adjust Pin Current Change		Io=10mA,Vin-Vo=1.5V,Tj=25		0.2	5	uA
Temperature Stability		Vin=3.3V,Vadj=0V,Io=10mA		0.5		%
Long Term Stability		Tj=125°C,1000 Hrs		0.3	1	%
RMS Output Noise		Tj=25°C 10hz<f<10khz		0.003		%Vo

Note 1 : Low duty cycle pulse testing with Kelvin connections are required in order to maintain accurate data.
Note 2 : Drop-out voltage is defined as the minimum differential voltage between Vin and Vout required to maintain regulation at Vout. It is measured when the output voltage drops 1% below its nominal value.

Note 3 : Minimum load current is defined as the minimum current required at the output in order for the output voltage to maintain regulation. Typically the resistor dividers are selected such that it automatically maintains this current.

PIN DESCRIPTIONS

PIN #	PIN SYMBOL	PIN DESCRIPTION
1	Adj	A resistor divider from this pin to the Vout pin and ground sets the output voltage.
2	Vout	The output of the regulator. A minimum of 10uF capacitor must be connected from this pin to ground to insure stability.
3	Vin	The input pin of the regulator. Typically a large storage capacitor is connected from this pin to ground to insure that the input voltage does not sag below the minimum drop out voltage during the load transient response. This pin must always be 1.3V higher than Vout in order for the device to regulate properly.

BLOCK DIAGRAM

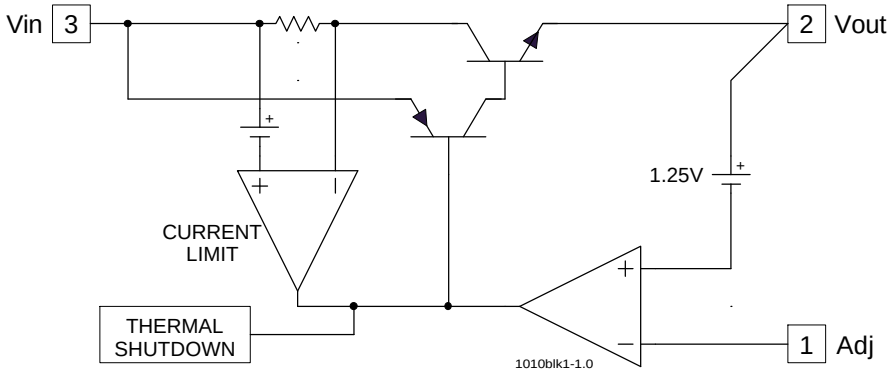

Figure 1 - Simplified block diagram of the US1010

APPLICATION INFORMATION

Introduction

The US1010 adjustable Low Dropout (LDO) regulator is a 3 terminal device which can easily be programmed with the addition of two external resistors to any voltages within the range of 1.25 to 5.5 V.This regulator unlike the first generation of the 3T regulators such as LM117 that required 3V differential between the input and the regulated output,only needs 1.3V differential to maintain output regulation. This is a key requirement for today'slow voltage IC applications that typically need 3.3V supply and are often generated from the 5V supply. Other applications such as high speed memory termination need to switch the load current from zero to full load in tens of nanoseconds at the their pins ,which translates to an approximately 300 to 500 nS current step at the regulator . In addition, the output voltage tolerances are sometimes tight and they include the transient response as part of the specification.

The US1010 is specifically designed to meet the fast current transient needs as well as providing an accurate initial voltage , reducing the overall system cost with the need for fewer output capacitors.

Output Voltage Setting

The US1010 can be programmed to any voltages in the range of 1.25V to 5.5V with the addition of R1 and R2 external resistors according to the following formula:

$$V_{OUT} = V_{REF}\left(1 + \frac{R_2}{R_1}\right) + I_{ADJ} \times R_2$$

Where : V_{REF} = 1.25V Typically

I_{ADJ} = 50 uA Typically

R_1 & R_2 as shown in figure 2

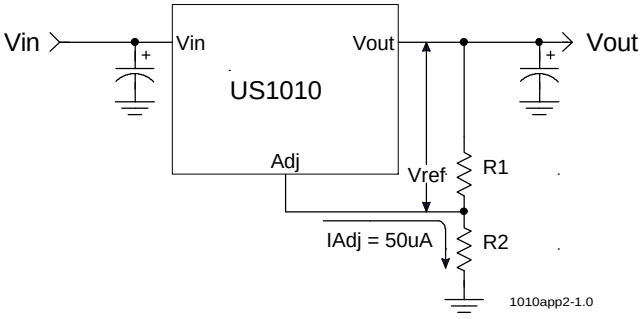

Figure 2 - Typical application of the US1010 for programming the output voltage.

The US1010 keeps a constant 1.25V between the output pin and the adjust pin. By placing a resistor R1 across these two pins a constant current flows through R1, adding to the Iadj current and into the R2 resistor producing a voltage equal to the (1.25/R1)*R2 + Iadj*R2 which will be added to the 1.25V to set the output voltage. This is summarized in the above equation. Since the minimum load current requirement of the US1010 is 10 mA , R1 is typically selected to be 121Ω resistor so that it automatically satisfies the minimum current requirement. Notice that since Iadj is typically in the range of 50uA it only adds a small error to the output voltage and should only be considered when a very precise output voltage setting is required. For example, in a typical 3.3V application where R1=121Ω and R2=200Ω the error due to Iadj is only 0.3% of the nominal set point.

Load Regulation

Since the US1010 is only a 3 terminal device , it is not possible to provide true remote sensing of the output voltage at the load.Figure 3 shows that the best load regulation is achieved when the bottom side of R2 is connected to the load and the top side of R1 resistor is connected directly to the case or the Vout pin of the regulator and not to the load. In fact , if R1 is connected to the load side, the effective resistance between the regulator and the load is gained up by the factor of (1+R2/R1) ,or the effective resistance will be ,Rp(eff)=Rp*(1+R2/R1).It is important to note that for high current applications, this can represent a significant percentage of the overall load regulation and one must keep the path from the regulator to the load as short as possible to minimize this effect.

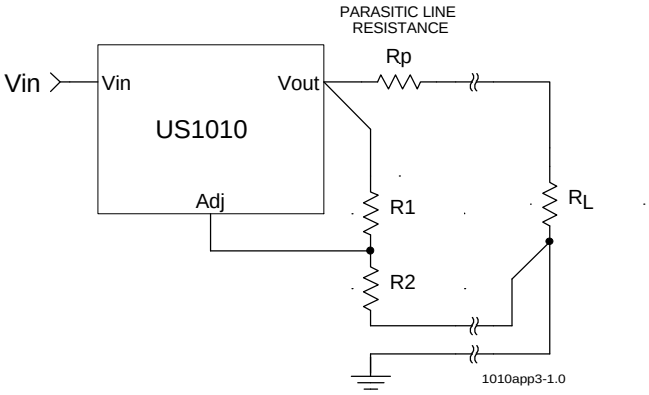

Figure 3 - Schematic showing connection for best load regulation

Stability

The US1010 requires the use of an output capacitor as part of the frequency compensation in order to make the regulator stable. Typical designs for microprocessor applications use standard electrolytic capacitors with a typical ESR in the range of 50 to 100 mΩ and an output capacitance of 500 to 1000uF. Fortunately as the capacitance increases, the ESR decreases resulting in a fixed RC time constant. The US1010 takes advantage of this phenomena in making the overall regulator loop stable.For most applications a minimum of 100uF aluminum electrolytic capacitor such as Sanyo MVGX series ,Panasonic FA series as well as the Nichicon PL series insures both stability and good transient response.

Thermal Design

The US1010 incorporates an internal thermal shutdown that protects the device when the junction temperature exceeds the maximum allowable junction temperature. Although this device can operate with junction temperatures in the range of 150°C ,it is recommended that the selected heat sink be chosen such that during maximum continuous load operation the junction temperature is kept below this number. The example below for a SCSI terminator application shows the steps in selecting the proper regulator in a surface mount package .(See US1015 for non surface mount packages)
Assuming the following specifications :

$V_{IN} = 5V$

$V_F = 0.5V$

$V_O = 2.85V$

$I_{OUTMAX} = 0.8A$

$T_A = 35°C$

Where ; V_F is the forward voltage drop of the D1diode as shown in figure 4.
The steps for selecting the right package with proper board area for heatsinking to keep the junction temperature below 135°C is given as :

1) Calculate the maximum power dissipation using :

$$P_D = I_{OUT} \times (V_{IN} - V_F - V_{OUT})$$

$$P_D = 0.8 \times (5 - 0.5 - 2.85) = 132\ W$$

2) Calculate the maximum θ_{JA} allowed for our example:

$$\theta_{JAMAX} = \frac{T_J - T_A}{P_D}$$

$$\theta_{JAMAX} = \frac{135 - 35}{132} = 75.6°C / W$$

2) Select a package from the datasheet with lower θ_{JA} than the one calculated in the previous step.

Selecting TO252 (D Pak) with at least 0.5" square of 0.062" FR4 board using 1OZ copper has 70°C/W which is lower than the calculated number.

To set the output DC voltage, we need to select R1 and R2 :

3) Assuming **R1=121 Ω , 1%**

$$R_2 = \left(\frac{V_{OUT}}{V_{REF}} - 1\right) \times 121 = \left(\frac{2.85}{125} - 1\right) \times 121 = 154.8\ \Omega$$

Select **R2=154 Ω ,1%**

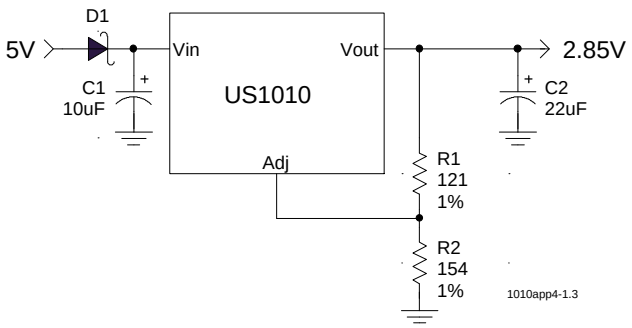

Figure 4 - Final Schematic for half of the GTL+ termination regulator.

Layout Consideration

The output capacitors must be located as close to the Vout terminal of the device as possible. It is recommended to use a section of a layer of the PC board as a plane to connect the Vout pin to the output capacitors to prevent any high frequency oscillation that may result due to excessive trace inductance.